



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Technology Center 2600

Applicant: Gerard Chauvel, Et al.

Serial No.: 09/890,894

Filed: 07/10/1997

For: Multiple processor Apparatus Having A Protocol
Processor Intended for the Execution Of A Collection
of Instructions In A Reduced Number of Operation

TI No.: 15767A

Art Unit: 2186

Examiner: Tran, Denise

Conf. No.: 5253

#31
JK
#0-23-a

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NOTICE OF APPEAL

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September 30, 2002

Assistant Commissioner For Patents

Washington, DC 20231

Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the US Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

Elizabeth Austin

Date

Applicant hereby appeals to the Board of Appeals from the decision dated September 4, 2002, of the Primary Examiner finally rejecting Claims 6-15, 17, and 34-39.

Charge the fee of \$320.00 to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

10/09/2002 MNEKONEN 00000084 200668 09890894

02 FC:119 320.00 CH

Respectfully submitted,

Ronald O. Neerings

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